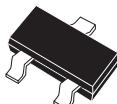


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CMPF4391  
CMPF4392  
CMPF4393

N-CHANNEL JFET



SOT-23 CASE

**MAXIMUM RATINGS** ( $T_A=25^\circ\text{C}$ )

	<b>SYMBOL</b>		<b>UNITS</b>
Drain-Gate Voltage	$V_{GD}$	40	V
Gate-Source Voltage	$V_{GS}$	40	V
Drain-Source Voltage	$V_{DS}$	40	V
Gate Current	$I_G$	50	mA
Power Dissipation	$P_D$	350	mW
Operating and Storage			
Junction Temperature	$T_J, T_{stg}$	-65 to +150	$^\circ\text{C}$
Thermal Resistance	$\Theta_{JA}$	357	$^\circ\text{C}/\text{W}$

**ELECTRICAL CHARACTERISTICS** ( $T_A=25^\circ\text{C}$  unless otherwise noted)

<b>SYMBOL</b>	<b>TEST CONDITIONS</b>	<b>CMPF4391</b>		<b>CMPF4392</b>		<b>CMPF4393</b>		<b>UNITS</b>
		<b>MIN</b>	<b>MAX</b>	<b>MIN</b>	<b>MAX</b>	<b>MIN</b>	<b>MAX</b>	
$I_{GSS}$	$V_{GS}=20\text{V}$		0.1		0.1		0.1	nA
$I_{GSS}$	$V_{GS}=20\text{V}, T_A=100^\circ\text{C}$		0.2		0.2		0.2	$\mu\text{A}$
$I_{DSS}$	$V_{DS}=20\text{V}$	50	150	25	75	5.0	30	mA
$I_{D(OFF)}$	$V_{DS}=20\text{V}, V_{GS}=12\text{V}$		0.1		-		-	nA
$I_{D(OFF)}$	$V_{DS}=20\text{V}, V_{GS}=7.0\text{V}$		-		0.1		-	nA
$I_{D(OFF)}$	$V_{DS}=20\text{V}, V_{GS}=5.0\text{V}$		-		-		0.1	nA
$I_{D(OFF)}$	$V_{DS}=20\text{V}, V_{GS}=12\text{V}, T_A=100^\circ\text{C}$		0.2		-		-	$\mu\text{A}$
$I_{D(OFF)}$	$V_{DS}=20\text{V}, V_{GS}=7.0\text{V}, T_A=100^\circ\text{C}$		-		0.2		-	$\mu\text{A}$
$I_{D(OFF)}$	$V_{DS}=20\text{V}, V_{GS}=5.0\text{V}, T_A=100^\circ\text{C}$		-		-		0.2	$\mu\text{A}$
$BV_{GSS}$	$I_G=1.0\mu\text{A}$	40		40		40		V
$V_{GS(OFF)}$	$V_{DS}=20\text{V}, I_D=1.0\text{nA}$	4.0	10	2.0	5.0	0.5	3.0	V
$V_{GS(f)}$	$I_G=1.0\text{mA}$		1.0		1.0		1.0	V
$V_{DS(ON)}$	$I_D=12\text{mA}$		0.4		-		-	V
$V_{DS(ON)}$	$I_D=6.0\text{mA}$		-		0.4		-	V
$V_{DS(ON)}$	$I_D=3.0\text{mA}$		-		-		0.4	V

**Central**™  
Semiconductor Corp.

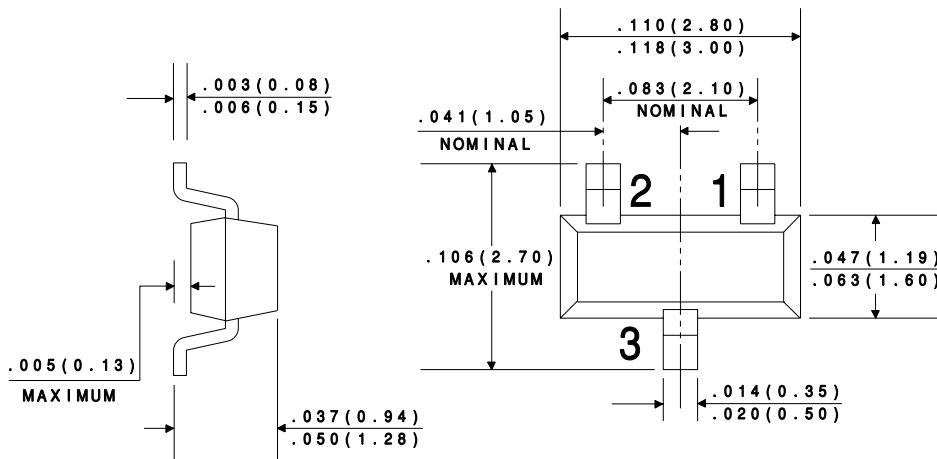
**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMPF4391 series types are N-Channel Silicon Field Effect Transistors manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for switching applications.

**Marking Codes are 6J, 6K, and 6G Respectively.**

SYMBOL	TEST CONDITIONS	CMPF4391		CMPF4392		CMPF4393		
		MIN	MAX	MIN	MAX	MIN	MAX	UNITS
r <sub>DS(ON)</sub>	I <sub>D</sub> =1.0mA, V <sub>GS</sub> =0	30	60			100	Ω	
r <sub>ds(ON)</sub>	V <sub>GS</sub> =0, I <sub>D</sub> =0, f=1.0kHz	30	60			100	Ω	
C <sub>iss</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0, f=1.0MHz	14	14			14	pF	
C <sub>rss</sub>	V <sub>GS</sub> =12V, V <sub>DS</sub> =0, f=1.0MHz	3.5	-			-	pF	
C <sub>rss</sub>	V <sub>GS</sub> =7.0V, V <sub>DS</sub> =0, f=1.0MHz	-	3.5			-	pF	
C <sub>rss</sub>	V <sub>GS</sub> =5.0V, V <sub>DS</sub> =0, f=1.0MHz	-	-			3.5	pF	
t <sub>ON</sub>	I <sub>D(ON)</sub> =12mA	15	-			-	ns	
t <sub>ON</sub>	I <sub>D(ON)</sub> =6.0mA	-	15			-	ns	
t <sub>ON</sub>	I <sub>D(ON)</sub> =3.0mA	-	-			15	ns	
t <sub>OFF</sub>	V <sub>GS(OFF)</sub> =12V	20	-			-	ns	
t <sub>OFF</sub>	V <sub>GS(OFF)</sub> =7.0V	-	35			-	ns	
t <sub>OFF</sub>	V <sub>GS(OFF)</sub> =5.0V	-	-			50	ns	

All dimensions in inches (mm).



#### LEAD CODE:

- 1) DRAIN
- 2) SOURCE
- 3) GATE